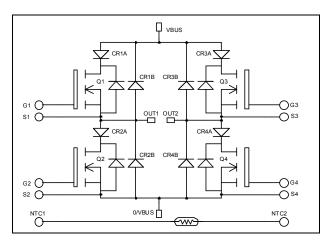
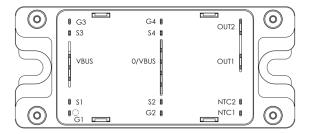


## Full bridge Series & parallel diodes MOSFET Power Module





# APTM50HM75STG

### $V_{DSS} = 500V$ $R_{DSon} = 75m\Omega \text{ typ } @ \text{ Tj} = 25^{\circ}C$ $I_{D} = 46A @ \text{ Tc} = 25^{\circ}C$

#### Application

- Motor control
  - Switched Mode Power Supplies
  - Uninterruptible Power Supplies

#### Features

- Power MOS 7<sup>®</sup> MOSFETs
  - Low R<sub>DSon</sub>
    - Low input and Miller capacitance
    - Low gate charge
    - Avalanche energy rated
    - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - Lead frames for power connections
- Internal thermistor for temperature monitoring
- High level of integration

#### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS compliant

### All ratings (a) $T_i = 25^{\circ}C$ unless otherwise specified

### Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
V <sub>DSS</sub>	Drain - Source Breakdown Voltage		500	V
т	Continuous Durin Comment	$T_c = 25^{\circ}C$	46	
I <sub>D</sub>	Continuous Drain Current	$T_c = 80^{\circ}C$	34	А
I <sub>DM</sub>	Pulsed Drain current		184	
V <sub>GS</sub>	Gate - Source Voltage		±30	V
R <sub>DSon</sub>	Drain - Source ON Resistance		90	mΩ
PD	Maximum Power Dissipation	$T_c = 25^{\circ}C$	357	W
I <sub>AR</sub>	Avalanche current (repetitive and non repetitive)		46	А
E <sub>AR</sub>	Repetitive Avalanche Energy		50	mI
E <sub>AS</sub>	Single Pulse Avalanche Energy	iche Energy		mJ

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

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### **Electrical Characteristics**

Symbol	Characteristic	Test Conditions		Тур	Max	Unit
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 500V$ $T_j = 25^{\circ}C$			100	
		$V_{GS} = 0V, V_{DS} = 400V$ $T_j = 125^{\circ}C$			500	μA
R <sub>DS(on)</sub>	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 23A$		75	90	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2.5 \text{mA}$			5	V
I <sub>GSS</sub>	Gate – Source Leakage Current	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$			±100	nA

### **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C <sub>iss</sub>	Input Capacitance	$V_{GS} = 0V$		5600		
C <sub>oss</sub>	Output Capacitance	$V_{\rm DS} = 25 V$		1200		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1 MHz		90		
Qg	Total gate Charge	$V_{GS} = 10V$		123		
Q <sub>gs</sub>	Gate – Source Charge	$V_{Bus} = 250V$		33		nC
$Q_{gd}$	Gate – Drain Charge	$I_D = 46A$		65		
T <sub>d(on)</sub>	Turn-on Delay Time	Inductive switching @ 125°C		18		
Tr	Rise Time	$V_{GS} = 15V$		35		ns
T <sub>d(off)</sub>	Turn-off Delay Time	$V_{Bus} = 333V$ $I_D = 46A$		87		
$T_{\rm f}$	Fall Time	$R_G = 5\Omega$		77		
Eon	Turn-on Switching Energy	Inductive switching @ 25°C		755		T
E <sub>off</sub>	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 46A, R_G = 5\Omega$		726		μJ
Eon	Turn-on Switching Energy	Inductive switching @ 125°C		1241		т
$\mathrm{E}_{\mathrm{off}}$	Turn-off Switching Energy	$V_{GS} = 15V, V_{Bus} = 333V$ $I_D = 46A, R_G = 5\Omega$		846		μJ
R <sub>thJC</sub>	Junction to Case Thermal Resistan	ice			0.35	°C/W

### Series diode ratings and characteristics

Symbol	Characteristic Test Conditions		Min	Тур	Max	Unit	
V <sub>RRM</sub>	Maximum Peak Repetitive Reverse Vol-	tage		600			V
I <sub>RM</sub>	Maximum Reverse Leakage Current	V <sub>R</sub> =600V				250	μA
I <sub>F</sub>	DC Forward Current		$T_c = 70^{\circ}C$		30		Α
	Diode Forward Voltage	$I_F = 30A$			1.6 1.8		
$V_{\rm F}$		$I_F = 60A$			1.9		V
		$I_F = 30A$	$T_{j} = 125^{\circ}C$		1.4		
4	$t_{\rm rr}$ Reverse Recovery Time		$T_j = 25^{\circ}C$		85		
ι <sub>rr</sub>		$T_{j} = 125^{\circ}C$		160		ns	
Qn	Reverse Recovery Charge	$\frac{v_{\rm R} - 400 v}{di/dt} = 200 \text{A}/\mu\text{s}$	$T_j = 25^{\circ}C$		130		nC
			$T_{j} = 125^{\circ}C$		700		ne
R <sub>thJC</sub>	Junction to Case Thermal Resistance					1.2	°C/W

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#### Parallel diode ratings and characteristics

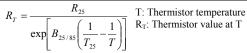
Symbol	Characteristic Test Conditions		Min	Тур	Max	Unit	
V <sub>RRM</sub>	Maximum Peak Repetitive Reverse Vol	tage		600			V
I <sub>RM</sub>	Maximum Reverse Leakage Current	$V_{R} = 600 V$				250	μA
$I_{\rm F}$	DC Forward Current		$T_c = 70^{\circ}C$		30		А
	Diode Forward Voltage	$I_F = 30A$			1.6	1.8	
$V_{\rm F}$		$I_F = 60A$			1.9		V
		$I_F = 30A$	$T_{j} = 125^{\circ}C$		1.4		
4	Reverse Recovery Time		$T_j = 25^{\circ}C$		85		
t <sub>rr</sub>		$I_F = 30A$ $V_R = 400V$	$T_{j} = 125^{\circ}C$		160		ns
Qn	Reverse Recovery Charge	$di/dt = 200 \text{ A}/\mu \text{ s}$	$T_j = 25^{\circ}C$		130		nC
			$T_{j} = 125^{\circ}C$		700		IIC
R <sub>thJC</sub>	Junction to Case Thermal Resistance					1.2	°C/W

### Thermal and package characteristics

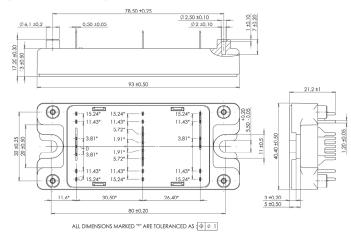
Symbol	Characteristic			Min	Max	Unit
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz					V
TJ	Operating junction temperature range				150	
T <sub>JOP</sub>	Recommended junction temperature under switching conditions			-40	T <sub>J</sub> max -25	°C
T <sub>STG</sub>	Storage Temperature Range			-40	125	C
T <sub>C</sub>	Operating Case Temperature				100	
Torque	Mounting torque	To heatsink	M5	2.5	4.7	N.m
Wt	Package Weight				160	g

### Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic			Min	Тур	Max	Unit
R <sub>25</sub>	Resistance @ 25°C	5°C			50		kΩ
$\Delta R_{25}/R_{25}$					5		%
B <sub>25/85</sub>	T <sub>25</sub> =298.15 K				3952		K
$\Delta B/B$			$T_{C}=100^{\circ}C$		4		%



### SP4 Package outline (dimensions in mm)



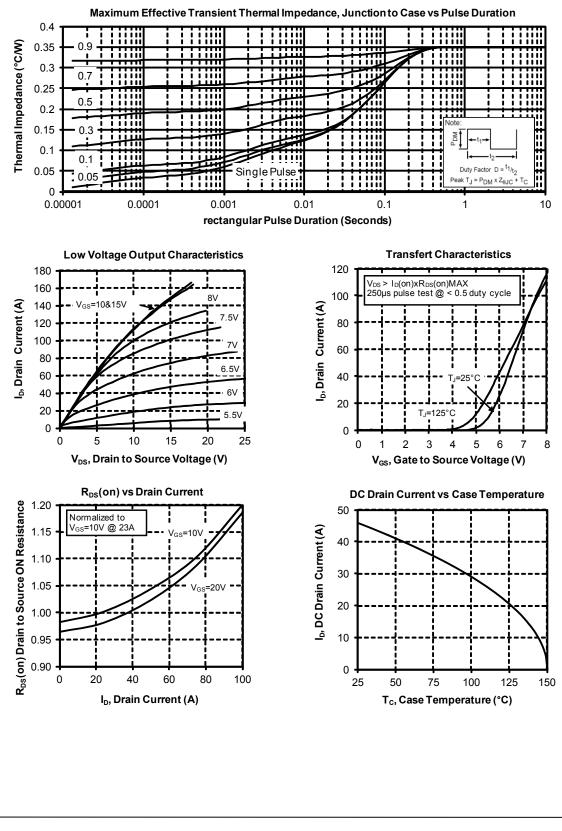
See application note APT0501 - Mounting Instructions for SP4 Power Modules on www.microsemi.com

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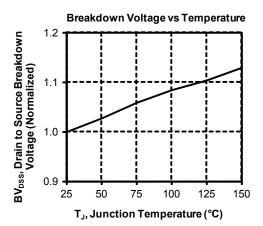
#### **Typical Performance Curve**



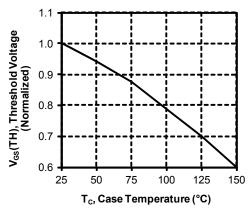
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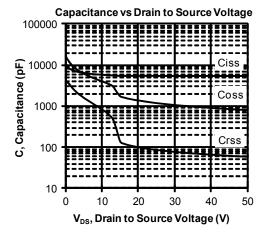
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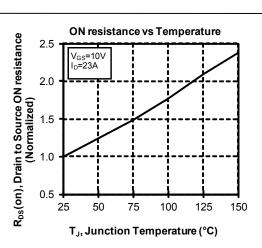




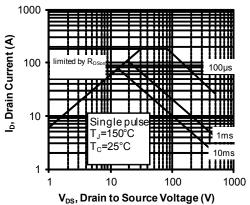


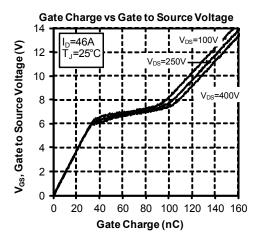








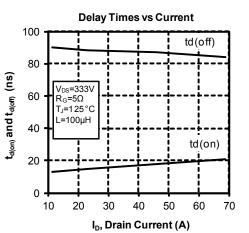


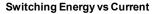


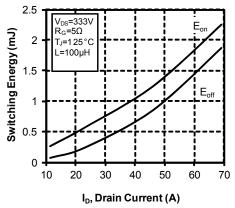
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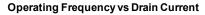
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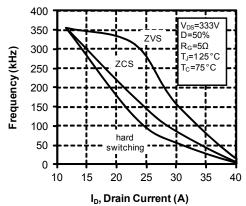




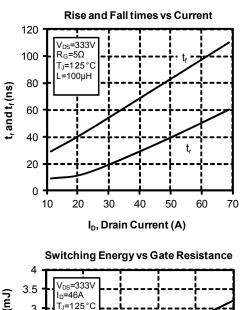


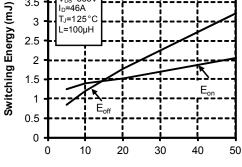




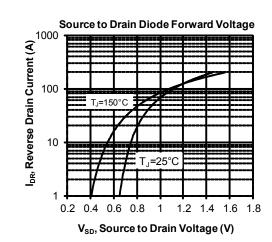












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