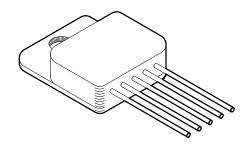


VERY HIGH CURRENT, LOW DROPOUT VOLTAGE REGULATORS

FEATURES:

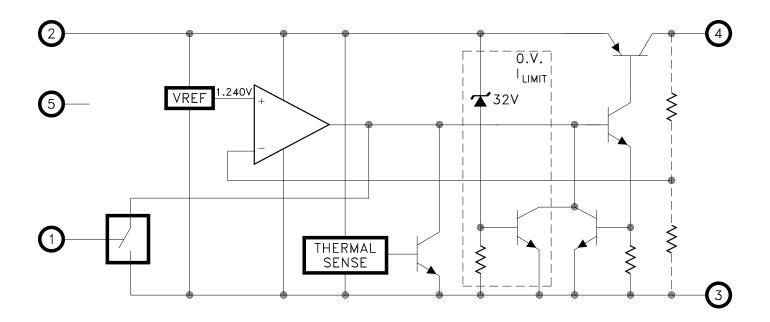
- · Electrically Isolated Top Tab or Z Tab SIP
- Extremely Low Dropout Voltage: 425mV @ 7.5 Amps
- Available in +1.5V,+1.7V,+1.9V,+2.5V,+3.3V,+5.0V and +12.0V
- · TTL Level Enable Pin: Zero Current Shutdown Mode
- · Reverse Battery and Load Dump Protection
- · Low Ground Current: 130mA Typical at Full Load
- 1% Maximum Guaranteed Accuracy
- · Output Current to 7.5 Amps



DESCRIPTION:

The MSK5176 series voltage regulators are available in +1.5V,+1.7V,+1.9V,+2.5V,+3.3V,+5.0V and +12.0V configurations. All boast ultra low dropout specifications due to the utilization of a super PNP output pass transistor with monolithic technology. Dropout voltages of 425mV at 7.5 amps are typical in this configuration, which drives efficiency up and power dissipation down. Accuracy is guaranteed with a 1% maximum output voltage tolerance. The series also offers a TTL/CMOS compatible on/off enable function. The MSK5176 series is packaged in a space efficient 5 pin power SIP available in two styles with three lead bend options.

EQUIVALENT SCHEMATIC



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TYPICAL APPLICATIONS

- · High Efficiency, Ultra High Current Linear Regulators
- · Constant Voltage/Current Regulators
- · System Power Supplies
- · Switching Power Supply Post Regulators
- Battery Powered Equipment

PIN-OUT INFORMATION

- 1 ENABLE
- 2 VIN
- 3 GROUND
- 4 VOUT
- 5 NC

CASE=ISOLATED

ABSOLUTE MAXIMUM RATINGS

VIN _P	Input Voltage (100mS 1%D.	C.)20V to +60V	Тѕт	Storage Temperature Range	e65°C to +150°C
VIN	Input Voltage	26V	T_LD	Lead Temperature	
V_{EN}	Enable Voltage	0.3V to 26V		(10 Seconds Soldering)	300°C
Іоит	Output Current	7.5A	TJ	Operating Temperature	
	•			MSK5176 Series	40°C to +85°C
				MSK5176H Series	-55°C to +125°C

ELECTRICAL SPECIFICATIONS

Parameter	Test Conditions ① ③		Group A	MSK5176H SERIES		MSK5176 SERIES				
Farameter			Subgroup	Min.	Тур.	Max.	Min.	Тур.	Max.	Units
Output Valtage Televane	Iouт=10mA; VIN=VOUT+1V		1	-	±0.5	±1.0	-	±0.5	±1.0	%
Output Voltage Tolerance			2,3	-	±1.0	±2.0	-	-	-	%
Dropout Voltage	② ΔVOUT=-1%; Ιουτ=250	Om A	1	-	80	200	-	80	225	mV
Dropout voltage	② ΔVOUT=-1%; Ιουτ=7	.5A	1	-	425	600	-	425	625	mV
Load Regulation ®	VIN=VOUT+1.5V		1	-	±0.2	±1.0	-	±0.2	±1.2	%
Load Regulation (8)	$10mA \le Iout \le 7A$		2,3	-	±0.3	±2.0	-	±0.3	-	%
Line Degulation	$(VOUT + 1V) \le VIN \le 2$	26V	1	1	±0.05	±0.5	-	±0.05	±0.6	%
Line Regulation	Iout=10mA		2,3	-	±0.5	±1.0	-	±0.5	-	%
Output Current Limit 29	utput Current Limit ② ⑨ VOUT=0V; VIN=VOUT+1V		-	-	9.5	15	-	9.5	15	Α
Ground Current ②	VIN=VOUT+1V; Iout=	=4A	-	-	45	85	-	45	90	mA
Ground Current 2	VIN=VOUT+1V; Iout=	VIN=VOUT+1V; IouT=7.5A		-	130	-	-	130	-	mA
Output Noise ② $CL=33\mu F; 10Hz \le f \le 100KHz$		-	-	260	-	-	260	-	μV	
Fachla lanut Valtana		HIGH/ON	1	2.4	1.2	-	2.4	1.2	-	٧
Enable Input Voltage ②		LOW/OFF	1	-	1.2	8.0	-	1.2	8.0	V
Footballer (O mod (2)		HIGH/ON	1	-	100	600	-	100	600	μA
Enable Input Current ②		LOW/OFF	1	-	-	2	-	-	2	μA
Shutdown Output Current ② VENABLE ≤ 0.8V		•	-	-	10	500	-	10	500	μA
Thermal Resistance 2	Thermal Resistance ② Junction to Case @ 125°C		-	-	0.9	1.3	-	0.9	1.5	°C/W
Thermal Shutdown ② TJ			-	-	135	-	-	135	-	°C

NOTES:

- ① Output decoupled to ground using $33\mu F$ minimum capacitance unless otherwise specified. ② This parameter is guaranteed by design but need not be tested.
- Typical parameters are representative of actual device performance but are for reference only.
- 3 All output parameters are tested using a low duty cycle pulse to maintain T_J = T_C.
- 4 Industrial grade devices shall be tested to subgroup 1 unless otherwise specified.
- (5) Military grade devices ('H' suffix) shall be 100% tested to subgroups 1,2 and 3.
- 6 Subgroup 1 Tc=+25°C Tc=+125°C Subgroup 2
- Tc=-55°C Subgroup 3
- Please consult the factory if alternate output voltages are required.
- ® Due to current limit, maximum output current may not be available at all values of VIN-VOUT and temperatures. See typical performance curves for clarification.
- (9) The output current limit function provides protection from transient overloads but it may exceed the maximum continuous rating. Continuous operation in current limit may damage the device.
- 10 Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.
- 1 Internal solder reflow temperature is 180°C, do not exceed.

APPLICATION NOTES

REGULATOR PROTECTION:

The MSK5176 series is fully protected against reversed input polarity, overcurrent faults, overtemperature conditions (Pd) and transient voltage spikes of up to 60V. If the regulator is used in dual supply systems where the load is returned to a negative supply, the output voltage must be diode clamped to ground.

OUTPUT CAPACITOR:

The output voltage ripple of the MSK5176 series voltage regulators can be minimized by placing a filter capacitor from the output to ground. The optimum value for this capacitor may vary from one application to the next, but a minimum of 33µF is recommended for optimum performance. This capacitor need not be an expensive low ESR type: aluminum electrolytics are adequate. In fact, extremely low ESR capacitors may contribute to instability. Tantalum capacitors are recommended for systems where fast load transient response is important. Transient load response can also be improved by placing a capacitor directly across the load.

LOAD CONNECTIONS:

In voltage regulator applications where very large load currents are present, the load connection is very important. The path connecting the output of the regulator to the load must be extremely low impedance to avoid affecting the load regulation specifications. Any impedance in this path will form a voltage divider with the load.

ENABLE PIN:

The MSK5176 series of voltage regulators are equipped with a TTL compatible ENABLE pin. A TTL high level on this pin activates the internal bias circuit and powers up the device. A TTL low level on this pin places the controller in shutdown mode and the device draws approximately 10µA of quiescent current. If the enable function is not used, simply connect the enable pin to the input.

DEVICE/CASE CONNECTION:

The MSK5176 series are highly thermally conductive devices and the thermal path from the package heat sink to the internal junctions is very short. Since the case is electrically isolated from the internal circuitry, the package can be directly connected to a heat sink.

HEAT SINK SELECTION:

To select a heat sink for the MSK5176, the following formula for convective heat flow may be used.

Governing Equation:

$$T_j = Pd x (R_{\theta jc} + R_{\theta cs} + R_{\theta sa}) + Ta$$

WHERE:

Tj = Junction Temperature

Pd = Total Power Dissipation

Rejc = Junction to Case Thermal Resistance

Recs = Case to Heat Sink Thermal Resistance

Resa = Heat Sink to Ambient Thermal Resistance

Ta = Ambient Temperature

First, the power dissipation must be calculated as follows:

Next, the user must select a maximum junction temperature. The absolute maximum allowable junction temperature is 125° C. The equation may now be rearranged to solve for the required heat sink to ambient thermal resistance (Resa).

EXAMPLE;

An MSK5176-3.3 is configured for VIN=+5V and VOUT=+3.3V. lout is a continuous 1A DC level. The ambient temperature is +25°C. The maximum desired junction temperature is 125°C.

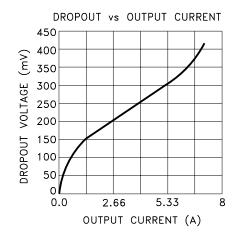
Rejc =
$$1.3^{\circ}$$
C/W and Recs = 0.15° C/W for most thermal greases
Power Dissipation = $(5V - 3.3V) \times (1A)$
= 1.7 Watts

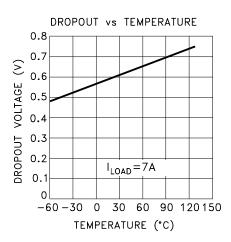
Solve for Resa:
Resa =
$$\left[\frac{125^{\circ}\text{C} - 25^{\circ}\text{C}}{1.7\text{W}}\right] - 1.3^{\circ}\text{C/W} - 0.15^{\circ}\text{C/W}$$

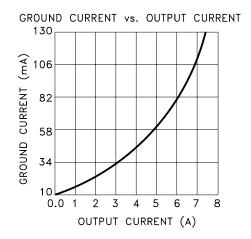
= 57.37°C/W

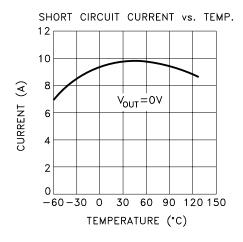
In this example, a heat sink with a thermal resistance of no more than 57°C/W must be used to maintain a junction temperature of no more than 125°C.

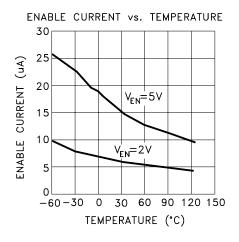
TYPICAL PERFORMANCE CURVES

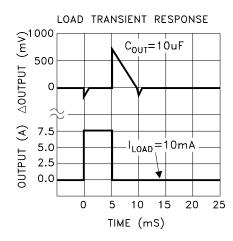


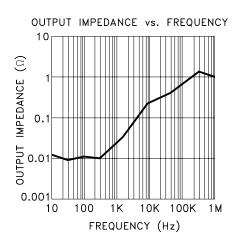


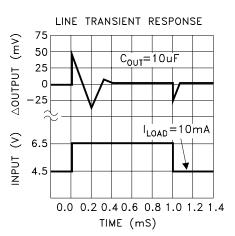


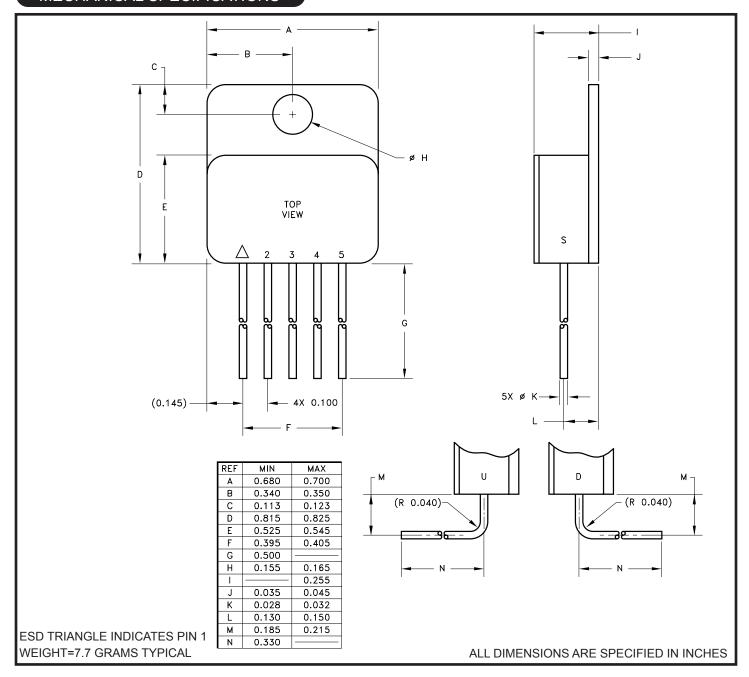




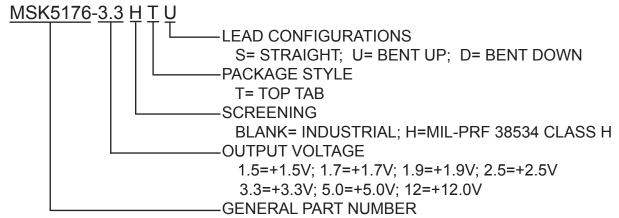




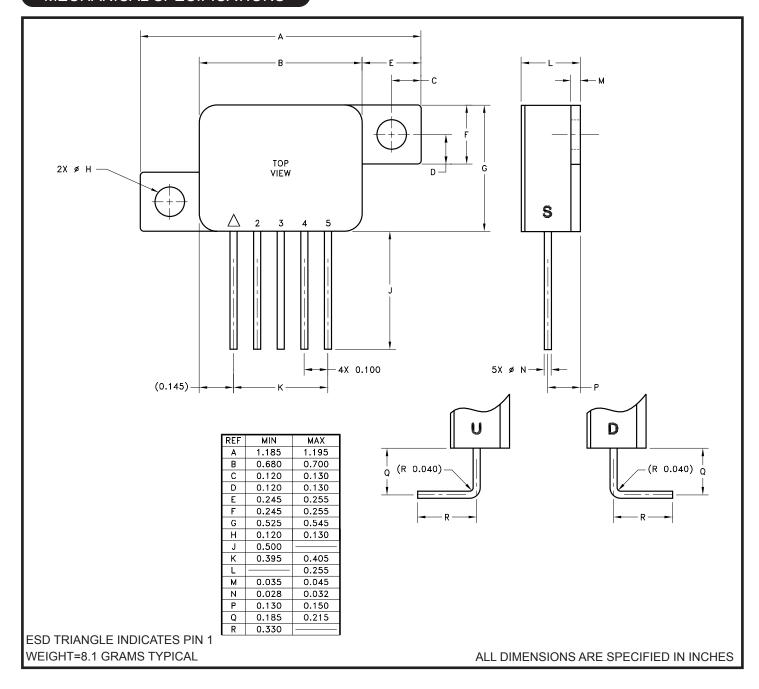




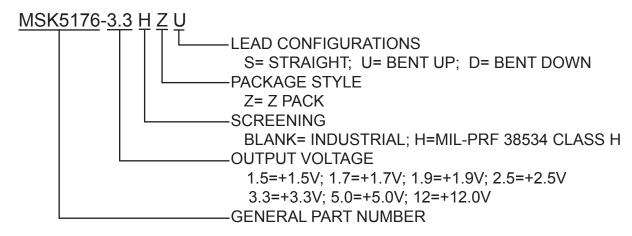
ORDERING INFORMATION



The above example is a +3.3V, Military regulator using the top tab package with leads bent up.



ORDERING INFORMATION



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REVISION HISTORY

REV	STATUS	DATE	DESCRIPTION	
D	Released	02/16	Add internal note and clarify mechanical specifications.	

MSK www.anaren.com/msk

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