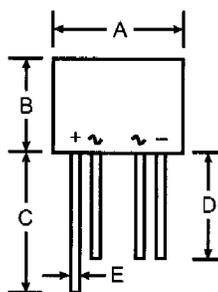


2W005G - 2W10G

2.0A GLASS PASSIVATED BRIDGE RECTIFIER

Features

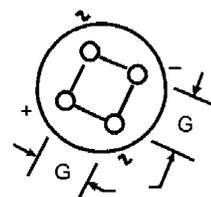
- Glass Passivated Die Construction
- Diffused Junction
- Low Forward Voltage Drop, High Current Capability
- Surge Overload Rating to 60A Peak
- Ideal for Printed Circuit Boards
- Case to Terminal Isolation Voltage 1500V
- Plastic Material: UL Flammability Classification Rating 94V-0
- UL Listed Under Recognized Component Index, File Number E94661



WOG		
Dim	Min	Max
A	8.84	9.86
B	4.00	4.60
C	27.90	—
D	25.40	—
E	0.71	0.81
G	4.60	5.60
All Dimensions in mm		

Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: As marked on Body
- Weight: 1.3 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



Maximum Ratings and Electrical Characteristics @ T_A = 25°C unless otherwise specified

Single phase, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	2W 005G	2W 01G	2W 02G	2W 04G	2W 06G	2W 08G	2W 10G	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	50	100	200	400	600	800	1000	V
Working Peak Reverse Voltage	V _{RWM}								
DC Blocking Voltage	V _R								
RMS Reverse Voltage	V _{R(RMS)}	35	70	140	280	420	560	700	V
Average Rectified Output Current @ T _A = 25°C	I _o	2.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load per element (JEDEC Method)	I _{FSM}	60							A
Forward Voltage (per element) @ I _F = 2.0A	V _{FM}	1.1							V
Peak Reverse Current @ T _A = 25°C at Rated DC Blocking Voltage @ T _A = 125°C	I _{RM}	5.0 500							μA
Typical Junction Capacitance (Note 2)	C _j	16							pF
Typical Thermal Resistance Junction to Case	R _{θJC}	40							°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-65 to +150							°C

- Notes: 1. Thermal resistance from junction to case mounted on PC board with 13 x 13mm (0.03mm thick) land areas.
2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

