650 V, 30 A Trench Field stop IGBTs with Fast Recovery Diode



KGF65A3H, MGF65A3H, FGF65A3H

Features

- Low Saturation Voltage
- High Speed Switching
- With Integrated Fast Recovery Diode
- RoHS Compliant

• V _{CE}	650 V
• $I_C (T_C = 100 ^{\circ}C)$	30 A
• Short circuit withstand time	10 μs
• V _{CE(sat)}	1.9 V typ.
• $t_f (T_i = 175 ^{\circ}C)$	60 ns typ.
• V _F	

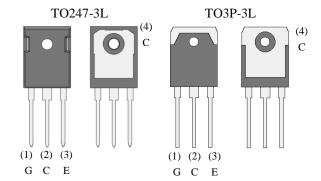
Applications

- Welding Converters
- Uninterruptible Power Supplies (UPS)
- PFC circuit
- Inverter circuit
- Bridge circuit

xGF65A3H Series

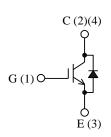
Products	KGF65A3H	MGF65A3H	FGF65A3H
Package	TO247-3L	TO3P-3L	TO3PF-3L

Package (Not to scale)





Equivalent circuit



Absolute Maximum Ratings

• Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Test conditions	Rating	Unit	Notes
Collector to Emitter Voltage	V_{CE}		650	V	
Gate to Emitter Voltage	V_{GE}		± 30	V	
Continuous Collector Current ⁽¹⁾	Ţ	T _C = 25 °C	50	A	
Continuous Conector Current	I_{C}	T _C = 100 °C	30	A	
Pulsed Collector Current	I _{C(PULSE)}	PW ≤ 1ms Duty cycle ≤ 1 %	90	A	
Diode Continuous Forward	Ţ	$T_C = 25 ^{\circ}C$	40(2)	A	
Current ⁽¹⁾	I_{F}	T _C = 100 °C	30	A	
Diode Pulsed Forward Current	I _{F(PULSE)}	PW ≤ 1ms Duty cycle ≤ 1 %	90	A	
Short Circuit Withstand Time	t _{SC}	$V_{GE} = 15 \text{ V},$ $V_{CE} = 400 \text{ V}$	10	μs	
Power Dissinction	D	T - 25 °C	217	W	MGF65A3H KGF65A3H
Power Dissipation P _D	$P_{\rm D}$ $T_{\rm C} = 25 ^{\circ}{\rm C}$	72	vv	FGF65A3H	
Operating Junction Temperature	T_{J}		175	°C	
Storage Temperature Range	T_{stg}		- 55 to 150	°C	

 $^{^{(1)}}$ I_C and I_F are limited by maximum junction temperature of TO3P-3L package.

⁽²⁾ Limited by bond wire.

KGF65A3H, MGF65A3H, FGF65A3H

Thermal Characteristics

• Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	Notes
Thermal Resistance of IGBT	$R_{\theta,IC}(IGBT)$	1	_	0.69	°C/W	MGF65A3H KGF65A3H	
(Junction to Case)	RejC(IODI)		-	-	2.08	C/ VI	FGF65A3H
Thermal Resistance of Diode	$R_{\theta JC}(Di)$		ı	-	1.15	°C/W	MGF65A3H KGF65A3H
(Junction to Case)	TtejC(D1)				2.28	<i>C/ </i> / / / / / / / /	FGF65A3H

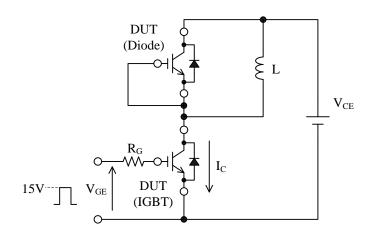
Electrical Characteristics

• Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Collector to Emitter Breakdown Voltage	V _{(BR)CES}	$I_C = 100 \ \mu A, \ V_{GE} = 0 \ V$	650	_	_	V
Collector to Emitter Leakage Current	I _{CES}	$V_{CE} = 650 \text{ V}, V_{GE} = 0 \text{ V}$	-	_	100	μΑ
Gate to Emitter Leakage Current	I_{GES}	$V_{GE} = \pm 30 \text{ V}$	_	_	± 500	nA
Gate Threshold Voltage	$V_{\text{GE(TH)}}$	$V_{CE} = 10 \text{ V}, I_{C} = 1 \text{ mA}$	4.0	5.5	7.0	V
Collector to Emitter Saturation Voltage	V _{CE(sat)}	$V_{GE} = 15 \text{ V}, I_{C} = 30 \text{ A}$	_	1.9	2.4	V
Input Capacitance	C_{ies}	$V_{CE} = 20 \text{ V}$	_	1800	_	
Output Capacitance	Coes	$V_{GE} = 0 V$	_	200	_	pF
Reverse Transfer Capacitance	C _{res}	f = 1.0 MHz	_	80	-	
Gate charge	Q_{g}	$V_{CE} = 520 \text{ V}, I_{C} = 30 \text{ A}$ $V_{GE} = 15 \text{ V}$	_	60	_	nC
Turn-On Delay Time	$t_{d(on)}$		_	30	_	
Rise Time	t _r		_	30	-	
Turn-Off Delay Time	t _{d(off)}	$T_i = 25 ^{\circ}C$	_	90	_	ns
Fall Time	t_{f}	Refer to Figure 1		30	_	
Turn-on energy*	E _{on}	_		0.5	_	
Turn-off energy	$E_{\rm off}$	_		0.4	_	mJ
Turn-On Delay Time	t _{d(on)}		_	30	_	
Rise Time	t _r		_	30	_	
Turn-Off Delay Time	$t_{ m d(off)}$	$T_i = 175 ^{\circ}\text{C}$	_	120	_	ns
Fall Time	$t_{ m f}$	Refer to Figure 1	_	60	_	
Turn-on energy*	E _{on}		_	1.0	_	T
Turn-off energy	$E_{\rm off}$		_	0.7	_	mJ
Emitter to Collector Diode Forward Voltage	V_F	I _F = 30 A	_	1.8	_	V
Emitter to Collector Diode Reverse Recovery Time	t _{rr}	$I_F = 30 \text{ A}$ $di/dt = 700 \text{ A/}\mu\text{s}$	_	50	_	ns

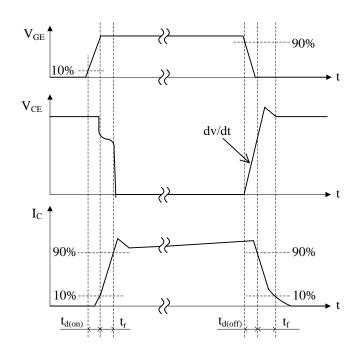
^{*}Energy losses include the reverse recovery of diode.

Test Circuits and Waveforms



Test conditions $V_{CE} = 400 \ V$ $I_C = 30 \ A$ $V_{GE} = 15 \ V$ $R_G = 10 \ \Omega$ $L = 100 \ \mu H$

(a) Test Circuit



(b) Waveform

Figure 1. Test Circuits and waveforms of dv/dt and Switching Time

Typical Characteristic Curves

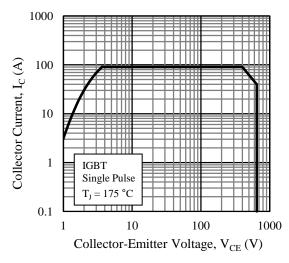


Figure 2. Reverse Bias Safe Operating Area

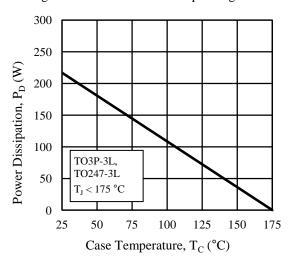


Figure 4. Power Dissipation vs. TO3P and TO247 Case Temperature

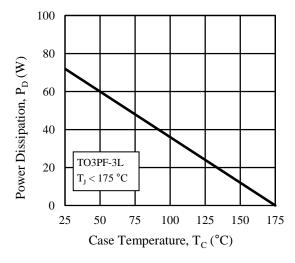


Figure 6. Power Dissipation vs. TO3PF Case Temperature

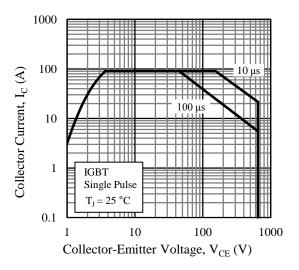


Figure 3. Safe Operating Area

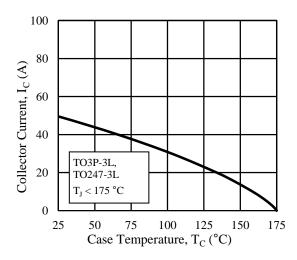


Figure 5. Collector Current vs. TO3P and TO247 Case Temperature

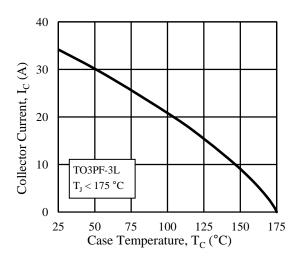


Figure 7. Collector Current vs. TO3PF Case Temperature

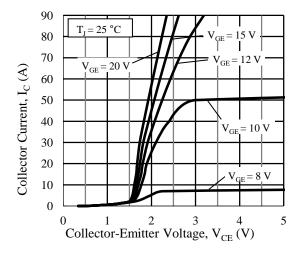


Figure 8. Output Characteristics ($T_J = 25$ °C)

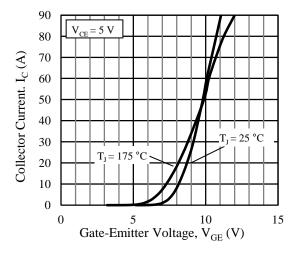


Figure 10. Transfer Characteristics

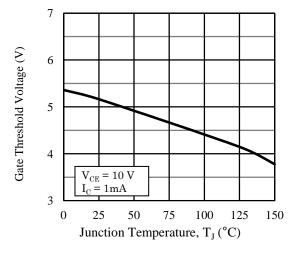


Figure 12. Gate Threshold Voltage vs. Junction Temperature

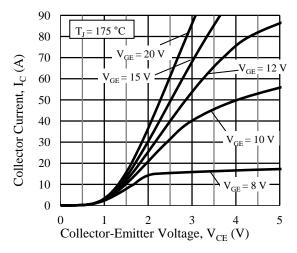


Figure 9. Output Characteristics (T_J = 175 °C)

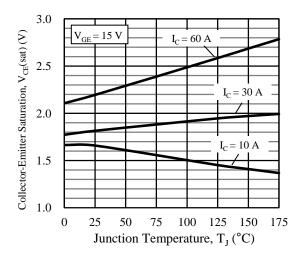


Figure 11. Saturation Voltage vs. Junction Temperature

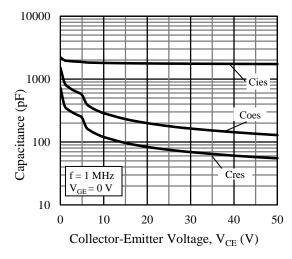


Figure 13. Capacitance Characteristics

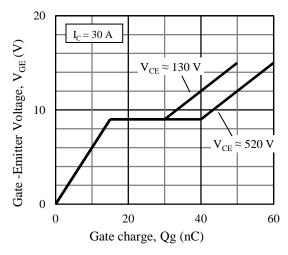


Figure 14. Typical Gate Charge

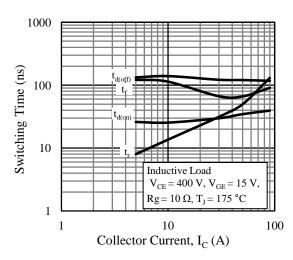


Figure 16. Switching Time vs. Collector Current

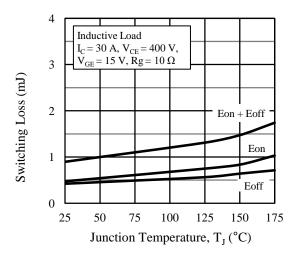


Figure 18. Switching Loss vs. Junction Temperature

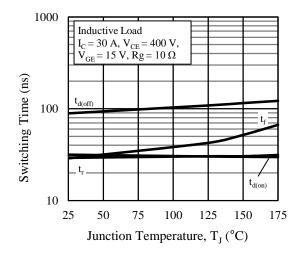


Figure 15. Switching time vs. Junction Temperature

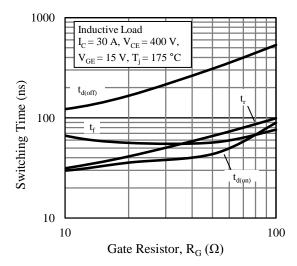


Figure 17. Switching Time vs. Gate Resistor

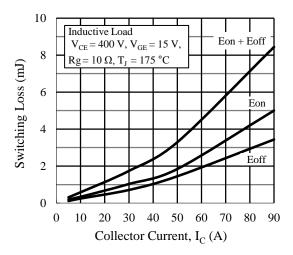


Figure 19. Switching Loss vs. Collector Current

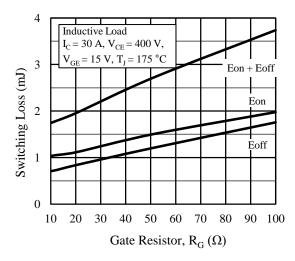


Figure 20. Switching Loss vs. Gate Resistor

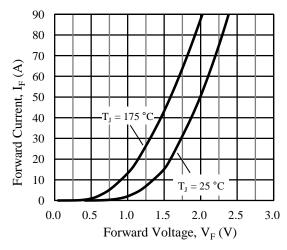


Figure 22. Diode Forward Characteristics

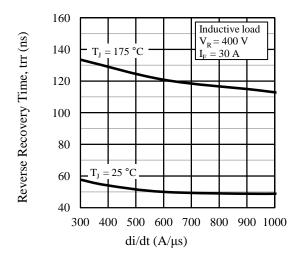


Figure 24. Diode Reverse Recovery Time vs. di/dt

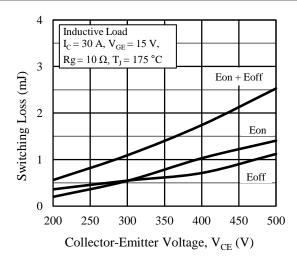


Figure 21. Switching Loss vs. Collector-Emitter Voltage

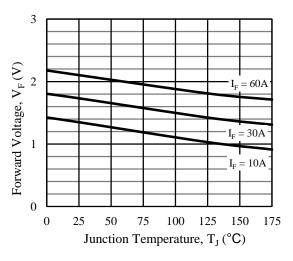


Figure 23. Diode Forward Voltage vs. Junction Temperature

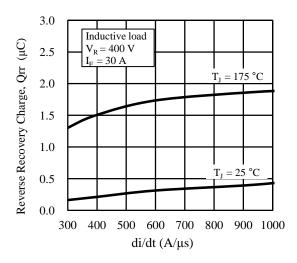


Figure 25. Diode Reverse Recovery Charge vs. di/dt

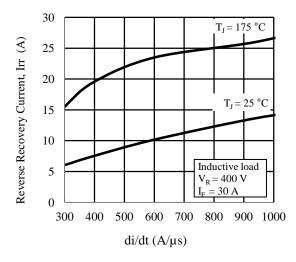


Figure 26. Diode Reverse Recovery Current vs. di/dt

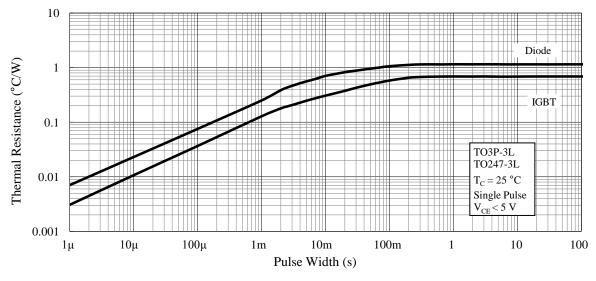


Figure 27. Transient Thermal Resistance (TO3P-3L and TO247-3L)

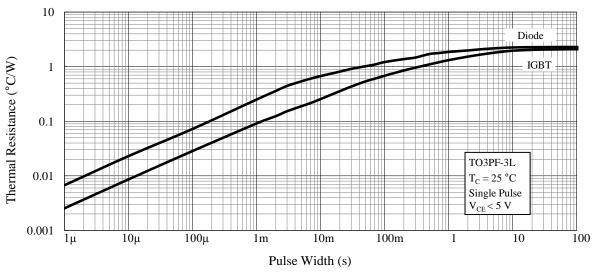
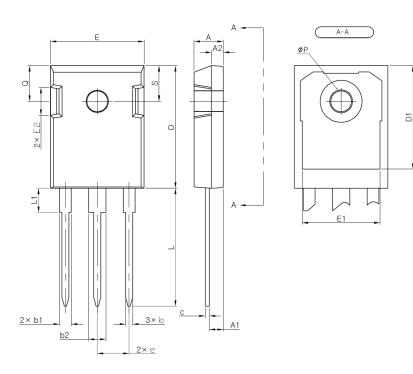


Figure 28. Transient Thermal Resistance (TO3PF-3L)

Package Outline

- Dimensions is in millimeters.
- Pin treatment Pb-free. Device composition compliant with the RoHS directive.

TO247-3L

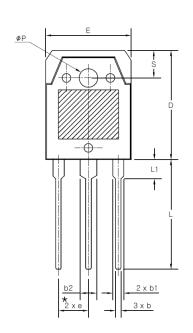


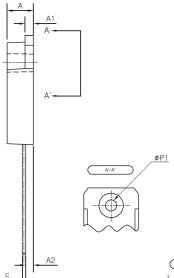
SYMBOL	MIN	NOM	MAX	
А	4.83	5.02	5.21	
A1	2.29	2.41	2.54	
A2	1.91	2.04	2.16	
р	1.14	1.27	1.40	
b1	1.91	2.10	2.20	
b2	2.92	3.10	3.20	
С	0.61	0.71	0.80	
D	20.80	21.07	21.34	
D1	17.43	17.63	17.83	
Е	15.75	15.94	16.13	
E1	13.06	13.26	13.46	
E2	4.32	4.58	4.83	
е	5.25	5.45	5.65	
L	19.81	20.19	20.57	
L1	3.81	4.07	4.32	
ΦP	3.55	3.60	3.65	
Q	5.59	5.90	6.20	
S	6.15 BSC			

NOTE

. THESE DIMENSION DO NOT INCLUDE MOLD PROTRUSION

TO3P-3L

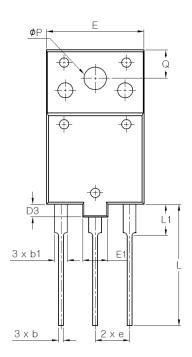


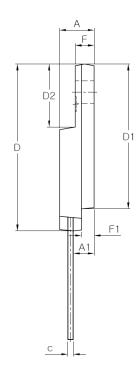


MIN	NOM	MAX
4.60	4.80	5.00
1.45	1.50	1.65
1.20	1.40	1.60
0.80	1.00	1.20
1.80	2.00	2.20
2.80	3.00	3.20
0.55	0.60	0.75
19.70	19.90	20.10
15.40	15.60	15.80
5.25	5.45	5.65
19.80	20.00	20.20
3.30	3.50	3.70
3.30	3.40	3.50
3.10	3.20	3.30
4.80	5.00	5.20
	4.60 1.45 1.20 0.80 1.80 2.80 0.55 19.70 15.40 5.25 19.80 3.30 3.30 3.10	4.60 4.80 1.45 1.50 1.20 1.40 0.80 1.00 1.80 2.00 2.80 3.00 0.55 0.60 19.70 19.90 15.40 15.60 5.25 5.45 19.80 20.00 3.30 3.50 3.30 3.40 3.10 3.20

1. THESE DIMENSIONS DO NOT INCLUDE PROTRUSIONS OF THE MOLD. 2. THE $^{\rm *}($)" MARK IS THE REFERENCE

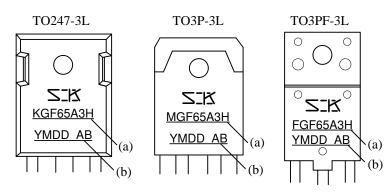
TO3PF-3L





SYMBOL	MIN	NOM	MAX
А	5.30	5.50	5.70
A1	3.10	3.30	3.50
b	0.65	0.75	0.95
b1	1.80	2.00	2.20
С	0.80	0.90	1.10
D	26.30	26.50	26.70
D1	22.80	23,00	23.20
D2	9.80	10,00	10.20
D3	1.80	2.00	2.20
Е	15.30	15.50	15.70
E1	3.80	4.00	4.20
е	5.25	5,45	5.65
F	2.80	3.00	3.20
F1	1.80	2.00	2.20
L	19.10	19.30	19.50
L1	4.80	5.00	5.20
Q	4.30	4.50	4.70
φP	3.40	3.60	3.80

Marking Diagram



- (a) Part Number
- (b) Lot Number

Y is the last digit of the year (0 to 9)

M is the month (1 to 9, O, N or D)

DD is the date (two digit of 01 to 31)

A and B are Sanken control number

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