

CDBDSC8650-G

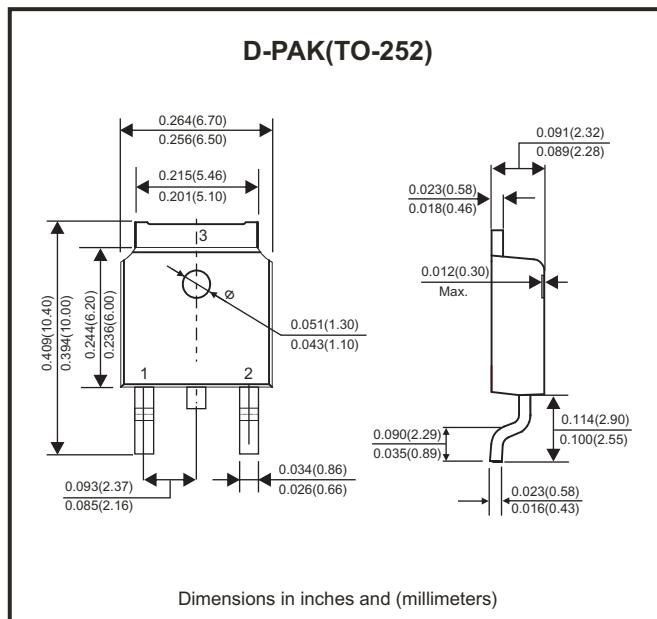
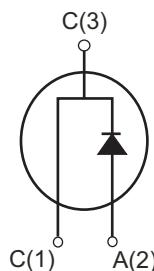
Reverse Voltage: 650 V
Forward Current: 8 A
RoHS Device



Features

- Rated to 650V at 8 Amps
- Short recovery time
- High speed switching possible
- High frequency operation.
- High temperature operation.
- Temperature independent switching behaviour.
- Positive temperature coefficient on VF

Circuit Diagram



Maximum Ratings (at TA=25°C, unless otherwise noted)

Parameter	Conditions	Symbol	Value	Unit
Repetitive peak reverse voltage		V _{RRM}	650	V
Surge peak reverse voltage		V _{RSM}	650	V
DC blocking voltage		V _{DC}	650	V
Continuous forward current	T _c = 25°C T _c = 135°C T _c = 150°C	I _F	25.5 11 8	A
Repetitive peak forward surge current	T _c = 25°C, tp = 10ms Half sine wave, D = 0.3	I _{FRM}	50	A
Non-repetitive peak forward surge current	T _c = 25°C, tp = 10ms Half sine wave	I _{FSM}	100	A
Power dissipation	T _c = 25°C T _c = 110°C	P _{TOT}	102.4 45	W
Typical thermal resistance	Junction to case	R _{θJC}	1.465	°C/W
Operating junction temperature range		T _J	-55 ~ +175	°C
Storage temperature range		T _{STG}	-55 ~ +175	°C

Silicon Carbide Power Schottky Diode

Comchip
SMD Diode Specialist

Electrical Characteristics (at $T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Conditions	Symbol	Min.	Typ.	Max.	Unit
Forward voltage	$I_F = 8\text{A}, T_J = 25^\circ\text{C}$	V_F		1.45	1.7	V
	$I_F = 8\text{A}, T_J = 175^\circ\text{C}$			1.75	2.5	
Reverse current	$V_R = 650\text{V}, T_J = 25^\circ\text{C}$	I_R		10	100	μA
	$V_R = 650\text{V}, T_J = 175^\circ\text{C}$			15	200	
Total capacitive charge	$V_R = 400\text{V}, T_J = 150^\circ\text{C}$ $Q_C = \int_0^{V_R} C(V) dV$	Q_C		30		nC
Total capacitance	$V_R = 0\text{V}, T_J = 25^\circ\text{C}, f = 1\text{MHz}$	C		550	588	pF
	$V_R = 200\text{V}, T_J = 25^\circ\text{C}, f = 1\text{MHz}$			56.5	57	
	$V_R = 400\text{V}, T_J = 25^\circ\text{C}, f = 1\text{MHz}$			54	54.5	

RATING AND CHARACTERISTIC CURVES (CDBDSC8650-G)

Fig.1 - Forward Characteristics

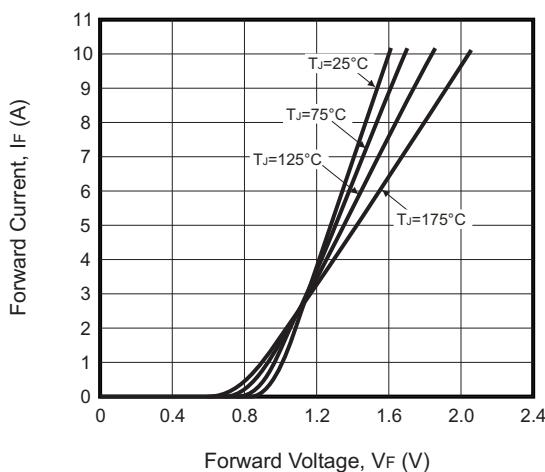


Fig.2 - Reverse Characteristics

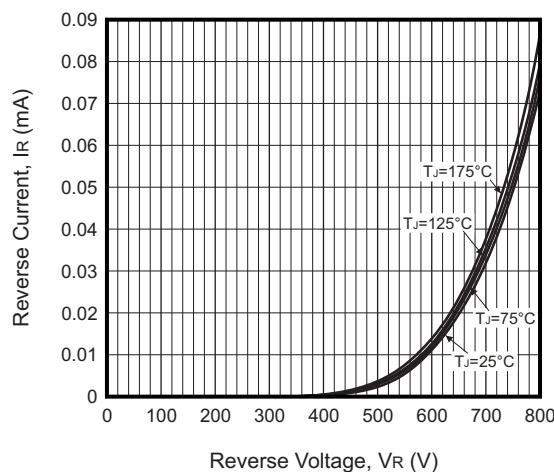


Fig.3 - Current Derating

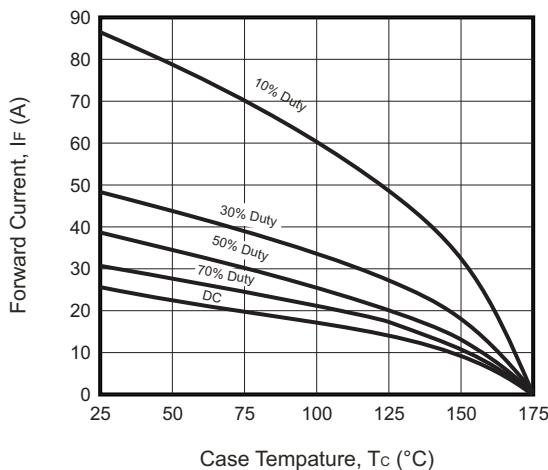
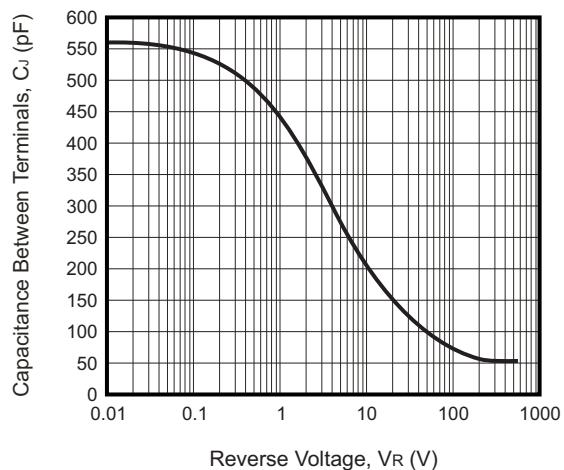


Fig.4 - Capacitance vs. Reverse Voltage



Company reserves the right to improve product design , functions and reliability without notice.

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REV:

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